

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al. Art Unit : 2811
Serial No. : 10/623,581 Examiner : Stanetta Isaac
Filed : July 22, 2003
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM
TRANSISTOR

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

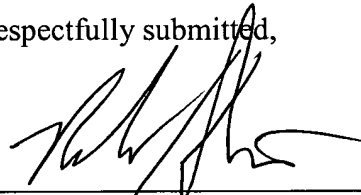
PETITION FOR ONE-MONTH EXTENSION OF TIME

Pursuant to 37 CFR §1.136, applicant hereby petitions that the period for response to the action dated July 13, 2005, be extended for one month to and including November 14, 2005.

Please apply the required fee of \$120 and any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 11/14/05



Roberto J. Devoto
Reg. No. 55,108

Fish & Richardson P.C.
1425 K Street, N.W.
11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331

40311302.doc

11/16/2005 MWOLDGE1 00000022 061050 10623581

01 FC:1251 120.00 DA